IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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ш	16	raiciii	Application	LOI

Confirmation No.: 7918

Kenichi KOYANAGI, et. al.

Date:

October 20, 2006

Serial No.:

10/630,444

Group Art Unit:

2823

Filed:

July 29, 2003

Examiner: William D. COLEMAN

For:

METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

VIA EFS-WEB

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT/SUBMISSION

Sir:

This is a response to the Final Office Action mailed August 3, 2006 in the above-identified application. Reconsideration of the application is respectfully requested.

FEE CALCULATION

Any additional fee required has been calculated as follows:

If checked, "Small Entity" status is claimed.

NO. CLAIMS

HIGHEST NO.

	AFTER				OUSL	Y					Α	DDIT.	
AMENDMENT				PAID FOR		EXTR	EXTRA PRESENT			RATE		FEE	
TOTAL		37	MINUS		37	* =	0	Х	(\$25	SE or \$50)	\$ 0		
INDEP.		3	MINUS		3	** =	0	X	(\$100	SE or \$200)	\$0		

^{*} not less than 20

FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM

TOTAL \$0

(\$180 SE or \$360)

If any additional payment is required, credit card payment in the amount of \$____ submitted via EFS-WEB.

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^{**} not less than 3